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These questions of speed, reliability, and power are all determined by the system-level electrical design of a digital system. Digital Systems Engineering presents a comprehensive treatment of these topics. It combines a rigorous development of the fundamental principles in each area with real-world examples of circuits and methods.

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Drafts of the book have been used to teach digital systems engineering courses at MIT (by Dally) and Washington University (by our colleague Fred Rosenberger). Starting with Autumn Quarter 1998, a course on digital systems engineering based on this book, EE273, will be offered at Stanford University.

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Kim J, Dally W, Scott S and Abts D Technology-Driven, Highly-Scalable Dragonfly Topology Proceedings of the 35th Annual International Symposium on Computer Architecture, (77-88) ... Chapter 1 is introductory; it discusses the purpose of digital systems engineering and provides a global overview of the problems it is designed to solve.

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Digital Systems Engineering | Rugged Electronics
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Digital Systems Engineering: Dally, William J. ...
A digital system represents information with discrete symbols (of which digits are a special case) rather than with a continuously varying quantity, as in an analog system. Most systems use just two symbols, often denoted by the binary digits (or bits) 0 and 1, to represent all information. Simple truth propositions are represented directly with a single bit, whereas strings of bits are used to represent more complex data.

INTRODUCTION TO DIGITAL SYSTEMS ENGINEERING (Chapter 1 ...
Textbook: Dally and Poulton, Digital Systems Engineering Grading. Approximate weighting for grade determination 25% Homework (exams based on homework) 35% First midterm exam 45% Final exam Collaboration: Academic integrity will be taken seriously. You may collaborate on homework with other students, use solutions

CSE464 Digital Systems Engineering
Why is Digital Systems Engineering Important? □ System-level electrical issues are becoming more critical - Higher clock rates □ wires are transmission lines □ clock skew and jitter are a major portion of a clock cycle □ many cables are more than one clock long

EE273 Lecture 1 Introduction to Digital Systems Engineering
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